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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TATSUYA OHGURO

: EXAMINER: FARAHANI, D.

SERIAL NO: 10/084,148

FILED: FEBRUARY 28, 2002

: GROUP ART UNIT: 2814

FOR:

SEMICONDUCTOR DEVICE

APPLIED TO A VARIABLE

CAPACITANCE CAPACITOR AND

AMPLIFIER

AMENDMENT UNDER 37 C.F.R. § 1.114

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Further in response to the Office Action dated December 11, 2002 and the Advisory Action dated April 3, 2003, please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel without prejudice Claims 3, 9 and 14.

Please amend Claims 1, 7, 13, and 16 as shown in clean form below. A marked up copy of the amended claims is attached.

- --1. (Amended) A semiconductor device comprising:
- a semiconductor substrate;
- a first conductivity type well area formed in a surface area of the semiconductor substrate;
 - a plurality of element isolation areas formed in the well area;